

Abstract

Circuit configuration and method for identifying error situations in interconnected systems

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The circuit configuration according to the invention comprises an electrical signal line loop, several partial systems connected thereto, which evaluate the state of the signal line loop, wherein a first selectable switching means is looped in between a first end of the signal line loop and a first voltage connection and a second selectable switching means is looped in
10 between a second end of the signal line loop and a second voltage connection, and a selection unit for selecting the first and second switching means. Use, e.g. in a fuel cell system.